PATENT

Attorney Docket No. MTI-31532

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Weimer, Ronald A.

Serial No.

09/941,827

Filing Date

August 29, 2001

For

Method of Improved High K Dielectric-Polysilicon Interface for CMOS Devices

Examiner:

HUYNH, Yennhu B.

Group Art Unit

2813

Confirmation No.:

7551

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Commissioner for Patents, Washington, D.C. 2023, as "Express Mail Post Office to Addressee" Mailing Label No. EV 326236245 US.

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE

Sir:

Applicant requests consideration of the pending claims in the above-identified patent application based on the remarks herein.

Remarks begin on page 2 of this paper.